

AFFILIATED INSTITUTIONS
ANNA UNIVERSITY CHENNAI :: CHENNAI 600 025
REGULATIONS - 2009
CURRICULUM I TO IV SEMESTERS (FULL TIME)
M.E. EMBEDDED SYSTEM TECHNOLOGIES

SEMESTER I

SL. No	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1	MA9211	Applied Mathematics for Electrical Engineers	3	1	0	4
2	ET9211	Advanced Digital System Design	3	0	0	3
3	ET9212	Micro Controller Based System Design	3	0	0	3
4	ET9213	Design of Embedded Systems	3	0	0	3
5	ET9214	Real Time Systems	3	0	0	3
6		Elective I	3	0	0	3
TOTAL			18	1	0	19

SEMESTER II

SL. NO	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1	ET9221	VLSI Architecture and Design Methodologies	3	1	0	4
2	ET9222	Real Time Operating System	3	0	0	3
3	ET9223	Embedded Networking	3	1	0	4
4	ET9224	Wireless & Mobile Communication	3	0	0	3
5		Elective II	3	0	0	3
6		Elective III	3	0	0	3
PRACTICAL						
7	ET9225	Embedded System Lab	0	0	3	2
TOTAL			18	2	3	22

SEMESTER III

SL. NO	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1		Elective IV	3	0	0	3
2		Elective V	3	0	0	3
3		Elective VI	3	0	0	3
PRACTICAL						
4	ET9231	Project Work (Phase I)	0	0	12	6
TOTAL			9	0	12	15

SEMESTER IV

SL. NO	COURSE CODE	COURSE TITLE	L	T	P	C
PRACTICAL						
1	ET9241	Project Work (Phase II)	0	0	24	12
TOTAL			0	0	24	12

TOTAL CREDITS TO BE EARNED FOR THE AWARD THE DEGREE = 68

ELECTIVES FOR M.E EMBEDDED SYSTEM TECHNOLOGIES

SEMESTER I

SL. NO	COURSE CODE	COURSE TITLE	L	T	P	C
1	ET9251	Software Technology for Embedded Systems	3	0	0	3
2	PE9275	Soft Computing Techniques	3	0	0	3
3	AP9222	Computer Architecture and parallel processing	3	0	0	3

SEMESTER II

4	ET9261	Design of Embedded Control Systems	3	0	0	3
5	ET9262	Embedded Communication and Software Design	3	0	0	3
6	ET9263	Ad-Hoc Networks	3	0	0	3
7	ET9264	Embedded Linux	3	1	0	4
8	ET9265	Digital Instrumentation	3	0	0	3
9	ET9266	RISC Processor Architecture and Programming	3	0	0	3

SEMESTER III

10	ET9271	Advanced Embedded Systems	3	0	0	3
11	ET9272	Advanced Digital Signal Processing	3	0	0	3
12	ET9273	Cryptography and Network Security	3	0	0	3
13	ET9274	Programming with VHDL	3	0	0	3
14	ET9275	Computer in Networking and Digital control	3	0	0	3
15	ET9276	Distributed Embedded Computing	3	0	0	3
16	ET9277	Principle of Robotics	3	0	0	3
17	ET9278	Application of MEMS Technology	3	0	0	3
18	ET9279	Digital Image Processing	3	0	0	3

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AIM

To expose the students to the fundamentals of digital logic based system design.

OBJECTIVES

To impart knowledge on

- i. Basics on Synchronous & Async digital switching design.
- ii. Design & realisation of error free functional blocks for digital systems

UNIT I SEQUENTIAL CIRCUIT DESIGN 9

Analysis of Clocked Synchronous Sequential Networks (CSSN) Modelling of CSSN – State Stable Assignment and Reduction – Design of CSSN – Design of Iterative Circuits – ASM Chart – ASM Realization, Design of Arithmetic circuits for Fast adder- Array Multiplier.

UNIT II ASYNCHRONOUS SEQUENTIAL CIRCUIT DESIGN 9

Analysis of Asynchronous Sequential Circuit (ASC) – Flow Table Reduction – Races in ASC – State Assignment Problem and the Transition Table – Design of ASC – Static and Dynamic Hazards – Essential Hazards – Data Synchronizers – Designing Vending Machine Controller – Mixed Operating Mode Asynchronous Circuits.

UNIT III FAULT DIAGNOSIS AND TESTABILITY ALGORITHMS 9

Fault Table Method – Path Sensitization Method – Boolean Difference Method – Kohavi Algorithm – Tolerance Techniques – The Compact Algorithm – Practical PLA's – Fault in PLA – Test Generation – Masking Cycle – DFT Schemes – Built-in Self Test.

UNIT IV SYNCHRONOUS DESIGN USING PROGRAMMABLE DEVICES 9

Programming Techniques -Re-Programmable Devices Architecture- Function blocks, I/O blocks, Interconnects, Realize combinational, Arithmetic, Sequential Circuit with Programmable Array Logic; Architecture and application of Field Programmable Logic Sequence.

UNIT V NEW GENERATION PROGRAMMABLE LOGIC DEVICES 9

Foldback Architecture with GAL, EPLD, EPLA, PEEL, PML; PROM – Realization State machine using PLD – FPGA – Xilinx FPGA – Xilinx 2000 - Xilinx 3000

TOTAL : 45 PERIODS

REFERENCES:

1. Donald G. Givone, "Digital principles and Design", Tata McGraw Hill 2002.
2. Stephen Brown and Zvonk Vranesic, "Fundamentals of Digital Logic with VHDL Design", Tata McGraw Hill, 2002
3. Mark Zwolinski, "Digital System Design with VHDL", Pearson Education, 2004
4. Parag K Lala, "Digital System design using PLD", BS Publications, 2003
5. John M Yarbrough, "Digital Logic applications and Design", Thomson Learning, 2001
6. Nripendra N Biswas, "Logic Design Theory", Prentice Hall of India, 2001

UNIT I	EMBEDDED DESIGN LIFE CYCLE	9
Product specification – Hardware / Software partitioning – Detailed hardware and software design – Integration – Product testing – Selection Processes – Microprocessor Vs Micro Controller – Performance tools – Bench marking – RTOS Micro Controller – Performance tools – Bench marking – RTOS availability – Tool chain availability – Other issues in selection processes.		
UNIT II	PARTITIONING DECISION	9
Hardware / Software duality – coding Hardware – ASIC revolution – Managing the Risk – Co-verification – execution environment – memory organization – System startup – Hardware manipulation – memory mapped access – speed and code density.		
UNIT III	INTERRUPT SERVICE ROUTINES	9
Watch dog timers – Flash Memory basic toolset – Host based debugging – Remote debugging – ROM emulators – Logic analyser – Caches – Computer optimisation – Statistical profiling		
UNIT IV	IN CIRCUIT EMULATORS	9
Buller proof run control – Real time trace – Hardware break points – Overlay memory – Timing constraints – Usage issues – Triggers.		
UNIT V	TESTING	9
Bug tracking – reduction of risks & costs – Performance – Unit testing – Regression testing – Choosing test cases – Functional tests – Coverage tests – Testing embedded software – Performance testing – Maintenance.		
		TOTAL : 45 PERIODS

REFERENCES

1. Arnold S. Berger – “Embedded System Design”, CMP books, USA 2002.
2. Sriram Iyer, “Embedded Real time System Programming”
3. ARKIN, R.C., Behaviour-based Robotics, The MIT Press, 1998.

UNIT I INTRODUCTION 9

Introduction – Issues in Real Time Computing – Structure of a Real Time System – Task classes – Performance Measures for Real Time Systems – Estimating Program Run Times – Task Assignment and Scheduling – Classical uniprocessor scheduling algorithms – Uniprocessor scheduling of IRIS tasks – Task assignment – Mode changes and Fault Tolerant Scheduling.

UNIT II PROGRAMMING LANGUAGES AND TOOLS 9

Programming Languages and Tools – Desired language characteristics – Data typing – Control structures – Facilitating Hierarchical Decomposition, Packages, Run time (Exception) Error handling – Overloading and Generics – Multitasking – Low level programming – Task Scheduling – Timing Specifications – Programming Environments – Run – time support.

UNIT III REAL TIME DATABASES 9

Real time Databases – Basic Definition, Real time Vs General Purpose Databases, Main Memory Databases, Transaction priorities, Transaction Aborts, Concurrency control issues, Disk Scheduling Algorithms, Two – phase Approach to improve Predictability – Maintaining Serialization Consistency – Databases for Hard Real Time Systems.

UNIT IV COMMUNICATION 9

Real – Time Communication – Communications media, Network Topologies Protocols, Fault Tolerant Routing. Fault Tolerance Techniques – Fault Types – Fault Detection. Fault Error containment Redundancy – Data Diversity – Reversal Checks – Integrated Failure handling.

UNIT V EVALUATION TECHNIQUES 9

Reliability Evaluation Techniques – Obtaining parameter values, Reliability models for Hardware Redundancy – Software error models. Clock Synchronization – Clock, A Nonfault – Tolerant Synchronization Algorithm – Impact of faults – Fault Tolerant Synchronization in Hardware – Fault Tolerant Synchronization in software.

TOTAL : 45 PERIODS**TEXT BOOKS**

1. C.M. Krishna, Kang G. Shin, "Real – Time Systems", McGraw – Hill International Editions, 1997.
2. Rajib Mall, "Real-time systems: theory and practice", Pearson Education, 2007
3. Peter D.Lawrence, "Real Time Micro Computer System Design – An Introduction", McGraw Hill, 1988.
4. Stuart Bennett, "Real Time Computer Control – An Introduction", Prentice Hall of India, 1998.
5. S.T. Allworth and R.N.Zobel, "Introduction to real time software design", Macmillan, 2nd Edition, 1987.
6. R.J.A Buhur, D.L Bailey, "An Introduction to Real – Time Systems", Prentice – Hall International, 1999.
7. Philip.A.Laplante, "Real Time System Design and Analysis", Prentice Hall of India, 3rd Edition, April 2004.

- UNIT I CMOS DESIGN 9**
Overview of digital VLSI design Methodologies- Logic design with CMOS-transmission gate circuits-Clocked CMOS-dynamic CMOS circuits, Bi-CMOS circuits- Layout diagram, Stick diagram-IC fabrications – Trends in IC technology.
- UNIT II PROGRAMABLE LOGIC DEVICES 12**
Programming Techniques-Anti fuse-SRAM-EPROM and EEPROM technology – Re-Programmable Devices Architecture- Function blocks, I/O blocks,Interconnects, Xilinx- XC9500,Cool Runner - XC-4000,XC5200, SPARTAN, Virtex - Altera MAX 7000-Flex 10K-Stratix.
- UNIT III ASIC CONSTRUCTION, FLOOR PLANNING, PLACEMENT AND ROUTING 6**
System partition – FPGA partitioning – Partitioning methods- floor planning – placement-physical design flow – global routing – detailed routing – special routing- circuit extraction – DRC.
- UNIT IV ANALOG VLSI DESIGN 6**
Introduction to analog VLSI- Design of CMOS 2stage-3 stage Op-Amp –High Speed and High frequency op-amps-Super MOS-Analog primitive cells-realization of neural networks.
- UNIT V LOGIC SYNTHESIS AND SIMULATION 12**
Overview of digital design with Verilog HDL, hierarchical modelling concepts, modules and port definitions, gate level modelling, data flow modelling, behavioural modelling, task & functions, Verilog and logic synthesis-simulation-Design examples,Ripple carry Adders, Carry Look ahead adders, Multiplier, ALU, Shift Registers, Multiplexer, Comparator, Test Bench.

L: 45 T:15=60 PERIODS**REFERENCES:**

- 1.M.J.S Smith, "Application Specific integrated circuits",Addition Wesley Longman Inc.1997.
- 2.Kamran Eshraghian,Douglas A.pucknell and Sholeh Eshraghian,"Essentials of VLSI circuits and system", Prentice Hall India,2005.
3. Wayne Wolf, " Modern VLSI design " Prentice Hall India,2006.
4. Mohamed Ismail ,Terri Fiez, "Analog VLSI Signal and information Processing", McGraw Hill International Editions,1994.
- 5.Samir Palnitkar, "Veri Log HDL, A Design guide to Digital and Synthesis" 2nd Ed,Pearson,2005.

UNIT I REVIEW OF OPERATING SYSTEMS 9

Basic Principles - Operating System structures – System Calls – Files – Processes – Design and Implementation of processes – Communication between processes – Introduction to Distributed operating system – Distributed scheduling.

UNIT II OVERVIEW OF RTOS 9

RTOS Task and Task state - Process Synchronisation- Message queues – Mail boxes - pipes – Critical section – Semaphores – Classical synchronisation problem – Deadlocks -

UNIT III REAL TIME MODELS AND LANGUAGES 9

Event Based – Process Based and Graph based Models – Real Time Languages – RTOS Tasks – RT scheduling - Interrupt processing – Synchronization – Control Blocks – Memory Requirements.

UNIT IV REAL TIME KERNEL 9

Principles – Design issues – Polled Loop Systems – RTOS Porting to a Target – Comparison and study of various RTOS like QNX – VX works – PSOS – C Executive – Case studies.

UNIT V RTOS APPLICATION DOMAINS 9

RTOS for Image Processing – Embedded RTOS for voice over IP – RTOS for fault Tolerant Applications – RTOS for Control Systems.

TOTAL : 45 PERIODS**REFERENCES:**

1. Raj Kamal, "Embedded Systems- Architecture, Programming and Design" Tata McGraw Hill, 2006.
2. Herma K., "Real Time Systems – Design for distributed Embedded Applications", Kluwer Academic, 1997.
3. Charles Crowley, "Operating Systems-A Design Oriented approach" McGraw Hill 1997.
4. C.M. Krishna, Kang, G.Shin, "Real Time Systems", McGraw Hill, 1997.
5. Raymond J.A.Bhur, Donald L.Bailey, "An Introduction to Real Time Systems", PHI 1999.
6. Mukesh Sigal and N G Shi "Advanced Concepts in Operating System", McGraw Hill 2000.

TEXT BOOKS

1. Frank Vahid, Givargis 'Embedded Systems Design: A Unified Hardware/Software Introduction', Wiley Publications
2. Jan Axelson, 'Parallel Port Complete', Penram publications
3. Dogan Ibrahim, 'Advanced PIC microcontroller projects in C', Elsevier 2008
4. Jan Axelson 'Embedded Ethernet and Internet Complete', Penram publications
5. Bhaskar Krishnamachari, 'Networking wireless sensors', Cambridge press 2005

ET 9224	WIRELESS AND MOBILE COMMUNICATION	L T P C 3 0 0 3
UNIT I	INTRODUCTION	9
Wireless Transmission – signal propagation – spread spectrum – Satellite Networks – Capacity Allocation – FAMA – DAMA – MAC		
UNIT II	MOBILE NETWORKS	9
Cellular Wireless Networks – GSM – Architecture – Protocols – Connection Establishment – Frequency Allocation – Routing – Handover – Security – GPRA		
UNIT III	WIRELESS NETWORKS	9
Wireless LAN – IEEE 802.11 Standard-Architecture – Services – AdHoc Network- Hiper Lan – Blue Tooth.		
UNIT IV	ROUTING	9
Mobile IP – DHCP – AdHoc Networks – Proactive and Reactive Routing Protocols – Multicast Routing		
UNIT V	TRANSPORT AND APPLICATION LAYERS	9
TCP over Adhoc Networks – WAP – Architecture – WWW Programming Model – WDP – WTLS – WTP – WSP – WAE – WTA Architecture – WML – WML scripts.		

TOTAL : 45 PERIODS

REFERENCES

1. Kaveh Pahlavan, Prasanth Krishnamoorthy, " Principles of Wireless Networks' PHI/Pearson Education, 2003
2. Uwe Hansmann, Lothar Merk, Martin S. Nicklons and Thomas Stober, " Principles of Mobile computing", Springer, New york, 2003.
3. C.K.Toh, " AdHoc mobile wireless networks", Prentice Hall, Inc, 2002.
4. Charles E. Perkins, " Adhoc Networking", Addison-Wesley, 2001.
5. Jochen Schiller, " Mobile communications", PHI/Pearson Education, Second Edition, 2003.
6. William Stallings, " Wireless communications and Networks", PHI/Pearson Education, 2002.

1. Design with 8 bit Microcontrollers 8051/PIC Microcontrollers
 - i) I/O Programming, Timers, Interrupts, Serial port programming
 - ii) PWM Generation, Motor Control, ADC/DAC, LCD and RTC Interfacing, Sensor Interfacing
 - iii) Both Assembly and C programming
2. Design with 16 bit processors
I/O programming, Timers, Interrupts, Serial Communication,
3. Design with ARM Processors.
I/O programming, ADC/DAC, Timers, Interrupts,
4. Study of one type of Real Time Operating Systems (RTOS)
5. Electronic Circuit Design of sequential, combinational digital circuits using CAD Tools
6. Simulation of digital controllers using MATLAB/LabVIEW .
7. Programming with DSP processors for
Correlation, Convolution, Arithmetic adder, Multiplier, Design of Filters - FIR based , IIR based
8. Design with Programmable Logic Devices using Xilinx/Altera FPGA and CPLD
Design and Implementation of simple Combinational/Sequential Circuits
9. Network Simulators
Simple wired/ wireless network simulation using NS2
10. Programming of TCP/IP protocol stack.

P = 45 TOTAL= 45 PERIODS

REFERENCE:

1. Mohamammad Ali Mazidi & Mazidi ‘ 8051 Microcontroller and Embedded Systems’, Pearson Education
2. Mohammad Ali Mazidi, Rolind Mckinley and Danny Causey, ‘PIC Microcontroller and Embedded Systems’ Pearson Education
3. Jan Axelson ‘Embedded Ethernet and Internet Complete’, Penram publications
4. Kraig Mitzner, ‘Complete PCB Design using ORCAD Capture and Layout’, Elsevier
5. Woon-Seng Gan, Sen M. Kuo, ‘Embedded Signal Processing with the Micro Signal Architecture’, John Wiley & Sons, Inc., Hoboken, New Jersey 2007
6. U. Meyer-Baese ‘Digital Signal Processing using Field Programmable Gate Arrays’, Springer
7. Dogan Ibrahim, ‘Advanced PIC microcontroller projects in C’, Elsevier 2008

ET 9231 PROJECT WORK (PHASE I) 0 0 12 6

ET 9241 PROJECT WORK (PHASE – II) 0 0 24 12

**ET 9251 SOFTWARE TECHNOLOGY FOR EMBEDDED SYSTEMS L T P C
3 0 0 3**

UNIT I PROGRAMMING EMBEDDED SYSTEMS 9

Embedded Program – Role of Infinite loop – Compiling, Linking and locating – downloading and debugging – Emulators and simulators processor – External peripherals – Toper of memory – Memory testing – Flash Memory.

UNIT II C AND ASSEMBLY 9

Overview of Embedded C - Compilers and Optimization - Programming and Assembly – Register usage conventions – typical use of addressing options – instruction sequencing – procedure call and return – parameter passing – retrieving parameters – everything in pass by value – temporary variables

UNIT III. EMBEDDED PROGRAM AND SOFTWARE DEVELOPMENT PROCESS 9

Program Elements – Queues – Stack- List and ordered lists-Embedded programming in C++ - Inline Functions and Inline Assembly - Portability Issues - Embedded Java- Software Development process: Analysis – Design- Implementation – Testing – Validation- Debugging - Software maintenance

UNIT IV UNIFIED MODELLING LANGUAGE 9

Object State Behaviour – UML State charts – Role of Scenarios in the Definition of Behaviour – Timing Diagrams – Sequence Diagrams – Event Hierarchies – Types and Strategies of Operations – Architectural Design in UML Concurrency Design – Representing Tasks – System Task Diagram – Concurrent State Diagrams – Threads. Mechanistic Design – Simple Patterns

UNIT V WEB ARCHITECTURAL FRAMEWORK FOR EMBEDDED SYSTEM 9

Basics – Client/sever model- Domain Names and IP address – Internet Infrastructure and Routing – URL – TCP/IP protocols - Embedded as Web Client - Embedded Web servers - HTML - Web security - Case study : Web-based Home Automation system.

TOTAL : 45 PERIODS

REFERENCES:

1. Kai Hwang "Advanced Computer Architecture". McGraw Hill International 2001.
2. Dezso Sima, Terence Fountain, Peter Kacsuk, "Advanced computer Architecture – A design Space Approach". Pearson Education, 2003.
3. Carl Homacher, Zvonko Vranesic, Sefwat Zaky, "Computer Organisation", 5th Edition, TMH, 2002.
4. David E. Culler, Jaswinder Pal Singh with Anoop Gupta "Parallel Computer Architecture", Elsevier, 2004.
5. John P. Shen. "Modern processor design Fundamentals of super scalar processors", Tata McGraw Hill 2003.
6. Sajjan G. Shiva "Advanced Computer Architecture", Taylor & Francis, 2008.
7. V.Rajaraman, C.Siva Ram Murthy, "Parallel Computers- Architecture and Programming", Prentice Hall India, 2008.
8. John L. Hennessy, David A. Petterson, "Computer Architecture: A Quantitative Approach", 4th Edition, Elsevier, 2007.
9. Harry F. Jordan Gita Alaghaband, "Fundamentals of Parallel Processing". Pearson Education, 2003.
10. Richard Y. Kain, "Advanced computer architecture – A system Design Approach", PHI, 2003.

ET 9261 DESIGN OF EMBEDDED CONTROL SYSTEM

L T P C
3 0 0 3

UNIT I	EMBEDDED SYSTEM ORGANIZATION	9
Embedded computing – characteristics of embedded computing applications – embedded system design challenges; Build process of Realtime Embedded system – Selection of processor; Memory; I/O devices-Rs-485, MODEM, Bus Communication system using I ² C, CAN, USB buses, 8 bit –ISA, EISA bus;		
UNIT II	REAL-TIME OPERATING SYSTEM	9
Introduction to RTOS; RTOS- Inter Process communication, Interrupt driven Input and Output -Nonmaskable interrupt, Software interrupt; Thread – Single, Multithread concept; Multitasking Semaphores.		
UNIT III	INTERFACE WITH COMMUNICATION PROTOCOL	9
Design methodologies and tools – design flows – designing hardware and software Interface . – system integration; SPI, High speed data acquisition and interface-SPI read/write protocol, RTC interfacing and programming;		
UNIT IV	DESIGN OF SOFTWARE FOR EMBEDDED CONTROL	9
Software abstraction using Mealy-Moore FSM controller, Layered software development, Basic concepts of developing device driver – SCI – Software - interfacing & porting using standard C & C++ ; Functional and performance Debugging with benchmarking Real-time system software – Survey on basics of contemporary RTOS – VXWorks, UC/OS-II		

UNIT IV CASE STUDIES WITH EMBEDDED CONTROLLER 9

Programmable interface with A/D & D/A interface; Digital voltmeter, control- Robot system; - PWM motor speed controller, serial communication interface.

TOTAL : 45 PERIODS

REFERENCES:

1. Steven F. Barrett, Daniel J. Pack, "Embedded Systems – Design and Applications with the 68HC 12 and HCS12", Pearson Education, 2008.
2. Raj Kamal, "Embedded Systems- Architecture, Programming and Design" Tata McGraw Hill, 2006.
3. Micheal Khevi, "The M68HC11 Microcontroller application in control,Instrumentation & Communication", PH NewJersy, 1997.
4. Muhammad Ali Mazidi, Rolin D. Mckinlay, Danny Causey, "PIC Microcontroller and Embedded Systems- Using Assembly and C for PIC18", Pearson Education,2008.
5. Steven F.Barrett,Daniel J.Pack,"Embedded Systems-Design & Application with the 68HC12 & HCS12", Pearson Education,2008.
6. Daniel W. Lewis, "Fundamentals of Embedded Software", Prentice Hall India, 2004.
7. Jack R Smith "Programming the PIC microcontroller with MBasic" Elsevier, 2007.
8. Keneth J.Ayala, "The 8086 Microprocessor: Programming & Interfacing the PC", Thomson India edition, 2007.

**ET 9262 EMBEDDED COMMUNICATION SOFTWARE DESIGN L T P C
3 0 0 3**

UNIT I OSI REFERENCE MODEL 9

Communication Devices – Communication Echo System – Design Consideration – Host Based Communication – Embedded Communication System – OS Vs RTOS.

UNIT II SOFTWARE PARTITIONING 9

Limitation of strict Layering – Tasks & Modules – Modules and Task Decomposition – Layer2 Switch – Layer3 Switch / Routers – Protocol Implementation – Management Types – Debugging Protocols.

UNIT III TABLES & OTHER DATA STRUCTURES 9

Partitioning of Structures and Tables – Implementation – Speeding Up access – Table Resizing – Table access routines – Buffer and Timer Management – Third Party Protocol Libraries.

UNIT IV MANAGEMENT SOFTWARE 9

Device Management – Management Schemes – Router Management – Management of Sub System Architecture – Device to manage configuration – System Start up and configuration.

UNIT V PERFORMANCE ANALYSIS

9

ABR beaconing, Performance parameters, Route-discovery time, End-to-end delay performance, Communication throughput performance, Packet loss performance, Route reconfiguration/repair time, TCP/IP based applications.

TOTAL : 45 PERIODS

REFERENCES

1. C. Siva Ram Murthy and B.S. Manoj, AdHoc Wireless Networks: Architectures and protocols, Prentice Hall PTR, 2004
2. C.-K.Toth, AdHoc Mobile Wireless Networks: Protocols and Systems, Prentice Hall PTR, 2001
3. Mohammad Ilyas, The Handbook of AdHoc Wireless Networks, CRC press, 2002
4. Charles E. Perkins, AdHoc Networking, Addison – Wesley, 2000
5. Stefano Basagni, Marco Conti, Silvia Giordano and Ivan Stojmenovic, Mobile AdHoc Networking, Wiley – IEEE press, 2004.

ET 9264

EMBEDDED LINUX

L T P C
3 1 0 4

AIM

To expose the students to the fundamentals of embedded Linux programming.

UNIT I FUNDAMENTALS OF OPERATING SYSTEMS 8

Overview of operating systems – Process and threads – Processes and Programs – Programmer view of processes – OS View of processes – Threads - Scheduling – Non preemptive and preemptive scheduling – Real Time Scheduling – Process Synchronization – Semaphores – Message Passing – Mailboxes – Deadlocks – Synchronization and scheduling in multiprocessor Operating Systems

UNIT II LINUX FUNDAMENTALS 10

Introduction to Linux – Basic Linux commands and concepts – Logging in - Shells - Basic text editing - Advanced shells and shell scripting – Linux File System –Linux programming - Processes and threads in Linux - Inter process communication – Devices – Linux System calls

UNIT III INTRODUCTION TO EMBEDDED LINUX 8

Embedded Linux – Introduction – Advantages- Embedded Linux Distributions - Architecture - Linux kernel architecture - User space – linux startup sequence - GNU cross platform Tool chain

UNIT IV BOARD SUPPORT PACKAGE AND EMBEDDED STORAGE 10

Inclusion of BSP in kernel build procedure - The bootloader Interface – Memory Map – Interrupt Management – PCI Subsystem – Timers – UART – Power Management – Embedded Storage – Flash Map – Memory Technology Device (MTD) – MTD Architecture - MTD Driver for NOR Flash – The Flash Mapping drivers – MTD Block and character devices – mtdutils package – Embedded File Systems – Optimizing storage space – Turning kernel memory

UNIT V EMBEDDED DRIVERS AND APPLICATION PORTING 9

Linux serial driver – Ethernet driver – I2C subsystem – USB gadgets – Watchdog timer – Kernel Modules – Application porting roadmap - Programming with pthreads – Operating System Porting Layer – Kernel API Driver - Case studies - RT Linux – uClinux.

L = 45 T = 15 TOTAL = 60

REFERENCE BOOKS

1. Dhananjay M. Dhamdhere, 'Operating Systems A concept based Approach', Tata Mcgraw-Hill Publishing Company Ltd
2. Matthias Kalle Dalheimer, Matt Welsh, 'Running Linux', O'Reilly Publications 2005
3. Mark Mitchell, Jeffrey Oldham and Alex Samuel 'Advanced Linux Programming' New Riders Publications
4. P. Raghavan ,Amol Lad , Sriram Neelakandan, 'Embedded Linux System Design and Development', Auerbach Publications 2006
5. Karim Yaghmour, 'Building Embedded Linux Systems', O'Reilly Publications 2003

ET 9265 DIGITAL INSTRUMENTATION

**L T P C
3 0 0 3**

UNIT I DATA ACQUISITION SYSTEMS 9

Overview of A/D converter, types and characteristics – Sampling , Errors. Objective – Building blocks of Automation systems –Counters – Modes of operation- Frequency, Period, Time interval measurements, Prescaler, Heterodyne converter for frequency measurement, Single and Multi channel Data Acquisition systems.

UNIT II INTERFACING AND DATA TRANSMISSION 9

Data transmission systems – 8086 Microprocessor based system design – Peripheral Interfaces – Time Division Multiplexing (TDM) – Digital Modulation – Pulse Modulation – Pulse Code Format – Interface systems and standards – Communications.

UNIT III INSTRUMENTATION BUS 9

Introduction, Modem standards, Basic requirements of Instrument Bus standards, Bus communication, interrupt and data handshaking , Interoperability, interchange ability for RS-232, USB, RS-422, RS-485.

UNIT IV PARALLEL PORT BUSES **9**
Field bus, Mod bus, GPIB, IEEE-488, VME, VXI, Network buses – Ethernet – TCP/IP protocols; CAN bus- basics, Message transfer, Fault confinement.

UNIT V CASE STUDIES **9**
PC based DAS, Data loggers, PC based industrial process measurements like flow, temperature, pressure and level development system, CRT interface and controller with monochrome and colour video display.

TOTAL : 45 PERIODS

REFERENCES:

1. A.J. Bouwens, "Digital Instrumentation" , TATA McGraw-Hill Edition, 1998.
2. N. Mathivanan, "Microprocessors, PC Hardware and Interfacing", Prentice-Hall India, 2005.
3. H S Kalsi, "Electronic Instrumentation" Second Edition, Tata McGraw-Hill,2006.
4. Joseph J. Carr, "Elements of Electronic Instrumentation and Measurement" Third Edition, Pearson Education, 2003.
5. Buchanan, "Computer busses", Arnold, London,2000.
6. Jonathan W Valvano, "Embedded Microcomputer systems", Asia Pvt. Ltd., Brooks/Cole, Thomson, 2001.

ET 9266 RISC PROCESSOR ARCHITECTURE AND PROGRAMMING **L T P C**
3 0 0 3

AIM

To expose the students to the fundamentals of AVR, ARM Architecture and Programming.

UNIT I AVR MICROCONTROLLER ARCHITECTURE **9**
Architecture – memory organization – addressing modes – instruction set – programming techniques –Assembly language & C programming- Development Tools – Cross Compilers – Hardware Design Issues .

UNIT II PERIPHERAL OF AVR MICROCONTROLLER **9**
I/O Memory – EEPROM – I/O Ports –SRAM –Timer –UART – Interrupt Structure- Serial Communication with PC – ADC/DAC Interfacing .

UNIT III ARM ARCHITECTURE AND PROGRAMMING **9**
Arcon RISC Machine – Architectural Inheritance – Core & Architectures -Registers – Pipeline - Interrupts – ARM organization - ARM processor family – Co-processors. Instruction set – Thumb instruction set – Instruction cycle timings - The ARM Programmer's model – ARM Development tools – ARM Assembly Language Programming and 'C'compiler programming.

UNIT III HARDWARE/SOFTWARE CO-SYNTHESIS 9

The Co-Synthesis Problem, State-Transition Graph, Refinement and Controller Generation, Distributed System Co-Synthesis.

UNIT IV MEMORY AND INTERFACING 9

Memory: Memory write ability and storage performance – Memory types – composing memory – Advance RAM interfacing communication basic – Microprocessor interfacing I/O addressing – Interrupts – Direct memory access – Arbitration multilevel bus architecture – Serial protocol – Parallel protocols – Wireless protocols – Digital camera example.

UNIT V CONCURRENT PROCESS MODELS AND HARDWARE SOFTWARE CO-DESIGN 9

Modes of operation – Finite state machines – Models – HCFSL and state charts language – state machine models – Concurrent process model – Concurrent process – Communication among process –Synchronization among process – Implementation – Data Flow model. Design technology – Automation synthesis – Hardware software co-simulation – IP cores – Design Process Model.

TOTAL : 45 PERIODS

REFERENCES

1. David. E. Simon, "An Embedded Software Primer", Pearson Education, 2001.
2. Tammy Noergaard, "Embedded System Architecture, A comprehensive Guide for Engineers and Programmers", Elsevier, 2006
3. Raj Kamal, "Embedded Systems- Architecture, Programming and Design" Tata McGraw Hill, 2006.
4. Frank Vahid and Tony Gwargie, "Embedded System Design", John Wiley & sons, 2002.
5. Steve Heath, "Embedded System Design", Elsevier, Second Edition, 2004.
6. Ralf Niemann, "Hardware/Software Co-Design for Data Flow Dominated Embedded Systems", Kluwer Academic Pub, 1998.
7. Jorgen Staunstrup, Wayne Wolf, "Hardware/Software Co-Design:Principles and Practice", Kluwer Academic Pub, 1997.
8. Giovanni De Micheli, Rolf Ernst Morgon, "Reading in Hardware/Software Co-Design" Kaufmann Publishers, 2001.

UNIT I	SYMMETRIC CIPHERS	9
Overview – classical Encryption Techniques – Block Ciphers and the Data Encryption standard – Introduction to Finite Fields – Advanced Encryption standard – Contemporary Symmetric Ciphers – Confidentiality using Symmetric Encryption.		
UNIT II	PUBLIC-KEY ENCRYPTION AND HASH FUNCTIONS	9
Introduction to Number Theory – Public-Key Cryptography and RSA – Key Management – Diffie-Hellman Key Exchange – Elliptic Curve Cryptography – Message Authentication and Hash Functions – Hash Algorithms – Digital Signatures and Authentication Protocols.		
UNIT III	NETWORK SECURITY PRACTICE	9
Authentication Applications – Kerberos – X.509 Authentication Service – Electronic mail Security – Pretty Good Privacy – S/MIME – IP Security architecture – Authentication Header – Encapsulating Security Payload – Key Management.		
UNIT IV	SYSTEM SECURITY	9
Intruders – Intrusion Detection – Password Management – Malicious Software – Firewalls – Firewall Design Principles – Trusted Systems.		
UNIT V	WIRELESS SECURITY	9
Introduction to Wireless LAN Security Standards – Wireless LAN Security Factors and Issues.		

TOTAL : 45 PERIODS

TEXT BOOKS

1. William Stallings, "Cryptography And Network Security – Principles And Practices", Pearson Education, 3rd Edition, 2003.

REFERENCES

1. Atul Kahate, "Cryptography and Network Security", Tata McGraw Hill, 2003.
2. Bruce Schneier, "Applied Cryptography", John Wiley and Sons Inc, 2001.
3. Stewart S. Miller, "Wi-Fi Security", McGraw Hill, 2003.
4. Charles B. Pfleeger, Shari Lawrence Pfleeger, "Security In Computing", 3rd Edition, Pearson Education, 2003.
5. Mai, "Modern Cryptography: Theory and Practice", First Edition, Pearson Education, 2003.

UNIT I	VHDL FUNDAMENTALS	9
Fundamental concepts- Modeling digital system-Domain and levels of modeling-modeling languages-VHDL modeling concepts-Scalar Data types and operations-constants and Variable-Scalar Types- Type Classification-Attributes and scalar types-expression and operators-Sequential statements.		
UNIT II	DATA TYPES AND BASIC MODELING CONSTRUCTS	9
Arrays- unconstrained array types-array operations and referencing- records - Access Types- Abstract Date types- -basic modeling constructs-entity declarations-Architecture bodies-behavioral description-structural descriptions- design Processing, case study: A pipelined Multiplier accumulator.		
UNIT III	SUBPROGRAMS , PACKAGES AND FILES	9
Procedures-Procedure parameters- Concurrent procedure call statements –Functions –Overloading –visibility of Declarations-packages and use clauses- Package declarations-package bodies-use clauses-Predefined aliases-Aliases for Data objects-Aliases for Non-Data items-Files- I/O-Files. Case study: A bit vector arithmetic Package.		
UNIT IV	SIGNALS, COMPONENTS, CONFIGURATIONS	9
Basic Resolved Signals-IEEE std_Logic_1164 resolved subtypes- resolved Signal Parameters - Generic Constants- Parameterizing behavior- Parameterizing structure-components and configurations-Generate Statements-Generating Iterative structure-Conditionally generating structure-Configuration of generate statements-case study: DLX computer Systems.		
UNIT V	DESIGN WITH PROGRAMMABLE LOGIC DEVICES	9
Realization of -Micro controller CPU.- Memories- I/O devices-MAC-Design,synthesis,simulation and testing.		

TOTAL : 45 PERIODS

REFERENCES

1. Peter J.Ashenden, "The Designer's guide to VHDL", Morgan Kaufmann publishers,San Francisco,Second Edition, May 2001.
2. Zainalabedin navabi, "VHDL Analysis and modeling of Digital Systems", McGraw Hill international Editions, Second Editions, 1998.
3. Charles H Roth, Jr. "Digital system Design using VHDL", Thomson ,2006.
4. Douglas Perry, "VHDL Programming by Example", Tata McGraw Hill,4th Edition 2002.
5. Navabi.Z., "VHDL Analysis and Modeling of Digital Systems", McGraw International, 1998.
6. Peter J Ashendem, "The Designers Guide to VHDL", Harcourt India Pvt Ltd, 2002
7. Skahill. K, "VHDL for Programmable Logic", Pearson education, 1996.

UNIT I NETWORK FUNDAMENTALS:**9**

Data communication networking – Data transmission concepts – Communication networking - Overview of OSI- TCP/IP layers – IP addressing - DNS – Packet Switching – Routing –Fundamental concepts in SMTP, POP, FTP, Telnet, HTML, HTTP, URL, SNMP,ICMP.

UNIT II DATA COMMUNICATION:**9**

Sensor data acquisition, Sampling, Quantization, Filtering ,Data Storage, Analysis using compression techniques, Data encoding – Data link control – Framing, Flow and Error control, Point to point protocol, Routers, Switches , Bridges – MODEMs, Network layer –Congestion control , Transport layer- Congestion control, Connection establishment.

UNIT III VIRTUAL INSTRUMENTATION**9**

Block diagram and Architecture – Data flow techniques – Graphical programming using GUI – Real time system – Embedded controller – Instrument drivers – Software and hardware simulation of I/O communication blocks – ADC/DAC – Digital I/O – Counter , Timer, Data communication ports.

UNIT IV MEASUREMENT AND CONTROL THROUGH INTERNET**9**

Web enabled measurement and control-data acquisition for Monitoring of plant parameters through Internet – Calibration of measuring instruments through Internet, Web based control – Tuning of controllers through Internet

UNIT V BASED MEASUREMENT AND CONTROL**9**

Simulation of signal analysis & controller logic modules for Virtual Instrument control – Case study of systems using VI for data acquisition, Signal analysis, controller design, Drives control.

TOTAL : 45 PERIODS**REFERENCES:**

1. Wayne Tomasi, "Introduction to Data communications and Networking" Pearson Education, 2007.
- 2..Al Williams, "Embedded Internet Design", Second Edition, TMH, 2007.
- 3.Douglas E.Comer, "Internetworking with TCP/IP, Vol. 1", Third Edition, Prentice Hall, 1999.
- 4.Cory L. Clark, "LabVIEW Digital Signal Processing and Digital Communication", TMH edition 2005.
- 5.Behrouza A Forouzan,"Data Communications and Networking" Fourth edition, TMH, 2007.
- 6.Krishna Kant,"Computer based Industrial control",PHI,2002.
- 7.Gary Johnson, "LabVIEW Graphical Programming", Second edition, McGraw Hill, Newyork, 1997.
8. Kevin James, "PC Interfacing and Data Acquisition: Techniques for measurement,

- Instrumentation and control, Newnes, 2000.
9. Cory L. Clark, "LabVIEW Digital Signal processing and Digital Communications" Tata McGRAW-HILL edition, 2005.

ET 9276 DISTRIBUTED EMBEDDED COMPUTING

L T P C
3 0 0 3

UNIT I THE HARDWARE INFRASTRUCTURE

9

Broad Band Transmission facilities – Open Interconnection standards – Local Area Networks – Wide Area Networks – Network management – Network Security – Cluster computers.

UNIT II INTERNET CONCEPTS

9

Capabilities and limitations of the internet – Interfacing Internet server applications to corporate databases HTML and XML Web page design and the use of active components.

UNIT III DISTRIBUTED COMPUTING USING JAVA

9

IO streaming – Object serialization – Networking – Threading – RMI – multicasting – distributed databases – embedded java concepts – case studies.

UNIT IV EMBEDDED AGENT

9

Introduction to the embedded agents – Embedded agent design criteria – Behaviour based, Functionality based embedded agents – Agent co-ordination mechanisms and benchmarks embedded-agent. Case study: Mobile robots.

UNIT V EMBEDDED COMPUTING ARCHITECTURE

9

Synthesis of the information technologies of distributed embedded systems – analog/digital co-design – optimizing functional distribution in complex system design – validation and fast prototyping of multiprocessor system-on-chip – a new dynamic scheduling algorithm for real-time multiprocessor systems.

TOTAL : 45 PERIODS

REFERENCES:

1. Dietel & Dietel, "JAVA how to program", Prentice Hall 1999.
2. Sape Mullender, "Distributed Systems", Addison-Wesley, 1993.
3. George Coulouris and Jean Dollimore, "Distributed Systems – concepts and design", Addison –Wesley 1988.

4. "Architecture and Design of Distributed Embedded Systems", edited by Bernd Kleinjohann C-lab, Universitat Paderborn, Germany, Kluwer Academic Publishers, Boston, April 2001, 248 pp.

ET 9277 **PRINCIPLES OF ROBOTICS** **L T P C**
3 0 0 3

UNIT I INTRODUCTION AND TERMINOLOGIES 9
Definition-Classification-History- Robots components-Degrees of freedom-Robot joints-coordinates- Reference frames-workspace-Robot languages-actuators-sensors-Position, velocity and acceleration sensors-Torque sensors-tactile and touch sensors-proximity and range sensors-social issues

UNIT II KINEMATICS 9
Mechanism-matrix representation-homogenous transformation-DH representation-Inverse kinematics-solution and programming-degeneracy and dexterity

UNIT III DIFFERENTIAL MOTION & VELOCITIES 9
Jacobian-differential motion of frames-Interpretation-calculation of Jacobian-Inverse Jacobian-Design-Lagrangian mechanics-dynamic equations-static force analysis

UNIT IV ROBOT CONTROL SYSTEM 9
Sensor characteristics- Hydraulic, Pneumatic and electric actuators-trajectory planning-decentralised PID control- non-linear decoupling control

UNIT V IMAGE PROCESSING & VISION SYSTEMS 9
Two and three dimensional images-spatial and frequency domain representation-noise and edges- convolution masks-Processing techniques-thresholding-noise reduction-edge detection-segmentation-Image analysis and object recognition

TOTAL : 45 PERIODS

REFERENCES

1. Saeed B. Niku , "Introduction to Robotics ", Pearson Education, 2002
2. Fu, Gonzalez and Lee Mcgrahill , "Robotics ", international
3. R.D. Klafter, TA Chmielewski and Michael Negin, "Robotic Engineering, An Integrated approach", Prentice Hall of India, 2003.

ET 9278 APPLICATIONS OF MEMS TECHNOLOGY **L T P C**
3 0 0 3

UNIT I MEMS: MICRO-FABRICATION, MATERIALS AND ELECTRO-MECHANICAL CONCEPTS 9
Overview of micro fabrication – Silicon and other material based fabrication processes – Concepts: Conductivity of semiconductors-Crystal planes and orientation-stress and strain-flexural beam bending analysis-torsional deflections-Intrinsic stress- resonant frequency and quality factor.

UNIT II ELECTROSTATIC SENSORS AND ACTUATION 9
Principle, material, design and fabrication of parallel plate capacitors as electrostatic sensors and actuators-Applications

UNIT III THERMAL SENSING AND ACTUATION 9
Principle, material, design and fabrication of thermal couples, thermal bimorph sensors, thermal resistor sensors-Applications.

REFERENCES :

1. Rafael C.Gonzalez and Richard E.Woods, "Digital Image Processing", 2nd Edition, Pearson Education, 2003.
2. Milan Sonka, Valclav Halavac and Roger Boyle, "Image Processing, Analysis and Machine Vision", 2nd Edition, Thomson Learning, 2001.
3. Anil K.Jain, "Fundamentals of Digital Image Processing". Pearson Education, 2003.